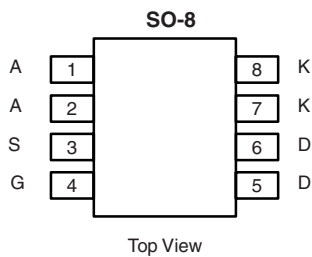


P-Channel 20-V (D-S) MOSFET with Schottky Diode

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)
- 20	0.210 at V _{GS} = - 4.5 V	- 2.7	2.9
	0.345 at V _{GS} = - 2.5 V	- 2.1	

SCHOTTKY PRODUCT SUMMARY		
V _{KA} (V)	V _F (V) Diode Forward Voltage	I _F (A)
20	0.50 V at 1.0 A	2.4



Ordering Information: Si4845DY-T1-E3 (Lead (Pb)-free)
Si4845DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

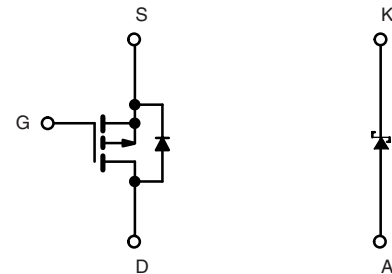
- Halogen-free According to IEC 61249-2-21 Definition
- LITTLE FOOT® Plus Integrated Schottky
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE
Available

APPLICATIONS

- Asynchronous dc-to-dc Buck



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage (MOSFET)	V _{DS}	- 20	V	
Reverse Voltage (Schottky)	V _{KA}	- 20		
Gate-Source Voltage (MOSFET)	V _{GS}	± 12		
Continuous Drain Current (T _J = 150 °C) (MOSFET)	I _D	T _C = 25 °C	- 2.7	
		T _C = 70 °C	- 2.1	
		T _A = 25 °C	- 2.1 ^{b, c}	
		T _A = 70 °C	- 1.7 ^{b, c}	
Pulsed Drain Current (MOSFET)	I _{DM}	- 7	A	
Continuous Source-Drain Diode Current (MOSFET Diode Conduction)	I _S	T _C = 25 °C		- 2.4
		T _A = 25 °C		- 1.9 ^{b, c}
Average Forward Current (Schottky)	I _F	1 ^b	A	
Pulsed Forward Current (Schottky)	I _{FM}	- 7		
Maximum Power Dissipation (Schottky)	P _D	T _C = 25 °C	2.75	
		T _C = 70 °C	1.75	
		T _A = 25 °C	1.75 ^{b, c}	
		T _A = 70 °C	1.1 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Typ.	Max.	Unit
Maximum Junction-to-Ambient (MOSFET and Schottky)	R _{thJA}	60	71.5	°C/W
Maximum Junction-to-Foot (Drain) (MOSFET and Schottky)	R _{thJF}	35	45	

Notes:

- Based on T_C = 25 °C.
- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under steady state conditions is 120 °C/W.

MOSFET SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	-20			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = -250\text{ }\mu\text{A}$		-25		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			2.6		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-0.5		-1.5	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 12\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20\text{ V}, V_{GS} = 0\text{ V}$			-1	μA
		$V_{DS} = -20\text{ V}, V_{GS} = 0\text{ V}, T_J = 75\text{ }^\circ\text{C}$			-10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq -5\text{ V}, V_{GS} = -4.5\text{ V}$	-5			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -4.5\text{ V}, I_D = -2\text{ A}$		0.175	0.210	Ω
		$V_{GS} = -2.5\text{ V}, I_D = -1.0\text{ A}$		0.285	0.345	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15\text{ V}, I_D = -2\text{ A}$		3.5		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = -10\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		312		μF
Output Capacitance	C_{oss}			63		
Reverse Transfer Capacitance	C_{rss}			33		
Total Gate Charge	Q_g	$V_{DS} = -10\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -4\text{ A}$		2.9	4.5	nC
Gate-Source Charge	Q_{gs}			0.72		
Gate-Drain Charge	Q_{gd}			0.65		
Gate Resistance	R_g	$f = 1\text{ MHz}$		5.5		Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -10\text{ V}, R_L = 2.5\text{ }\Omega$ $I_D \equiv -4\text{ A}, V_{GEN} = -4.5\text{ V}, R_g = 1\text{ }\Omega$		8	13	ns
Rise Time	t_r			40	60	
Turn-Off Delay Time	$t_{d(off)}$			17	26	
Fall Time	t_f			11	18	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -10\text{ V}, R_L = 2.5\text{ }\Omega$ $I_D \equiv -4\text{ A}, V_{GEN} = -10\text{ V}, R_g = 1\text{ }\Omega$		3	6	
Rise Time	t_r			10	16	
Turn-Off Delay Time	$t_{d(off)}$			12	20	
Fall Time	t_f			8	15	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$			-2.7	A
Pulse Diode Forward Current	I_{SM}				-7	
Body Diode Voltage	V_{SD}	$I_S = -1.9\text{ A}, V_{GS} = 0\text{ V}$		-0.85	-1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = -2\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		24	40	ns
Body Diode Reverse Recovery Charge	Q_{rr}			14	20	nC
Reverse Recovery Fall Time	t_a			14		ns
Reverse Recovery Rise Time	t_b			10		

Notes:

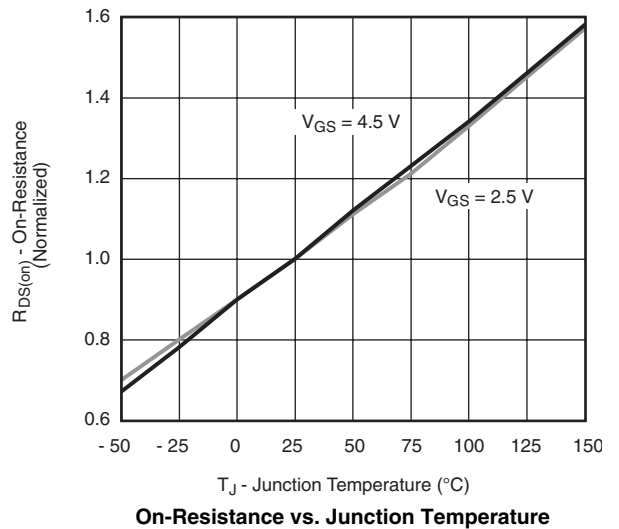
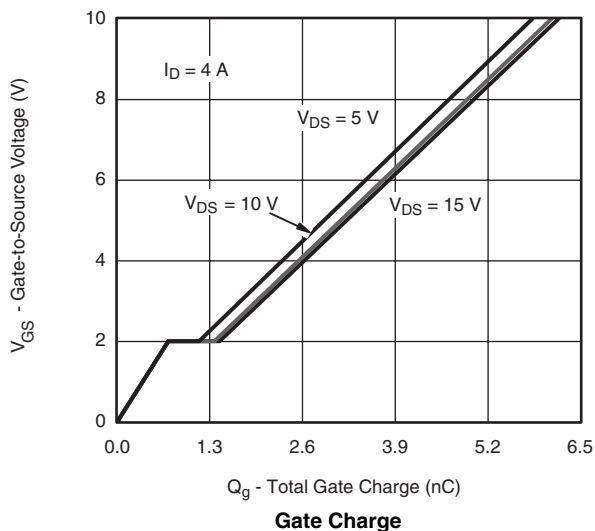
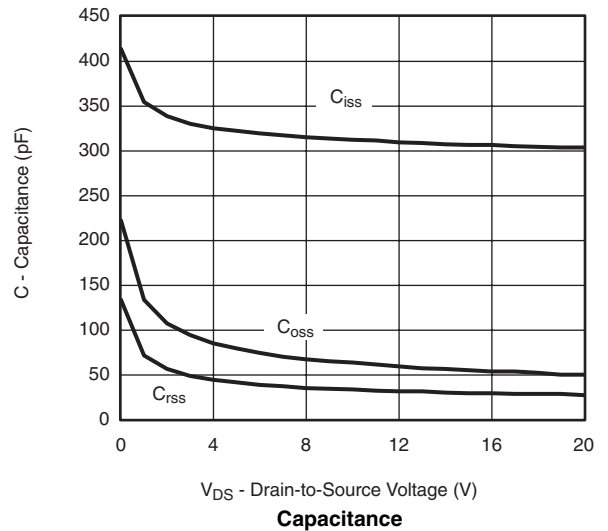
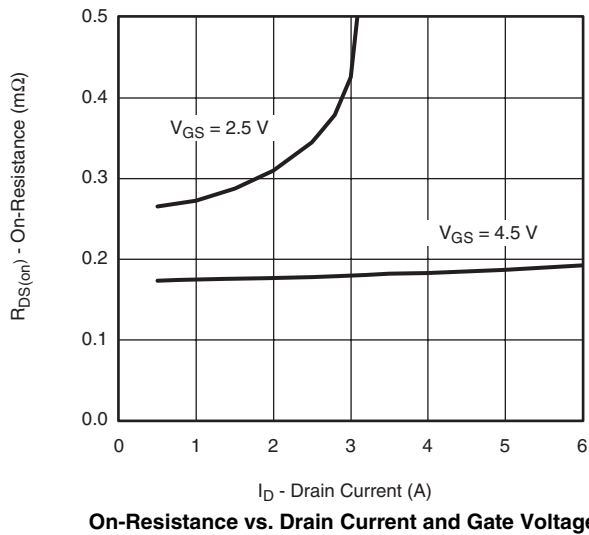
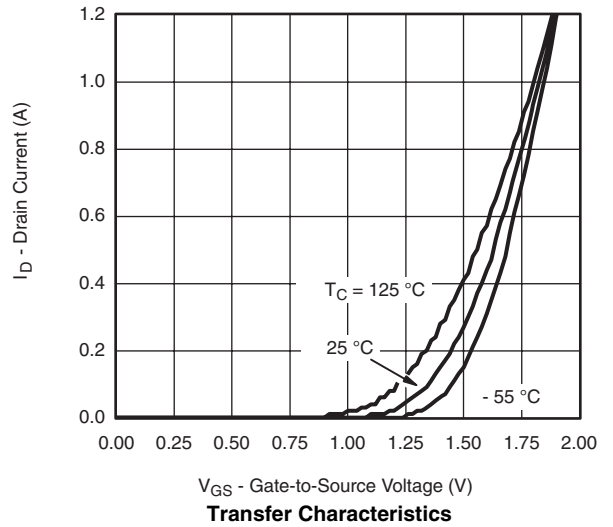
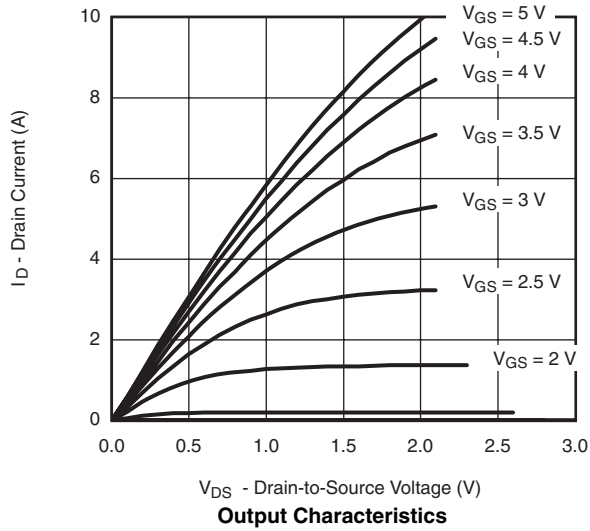
a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

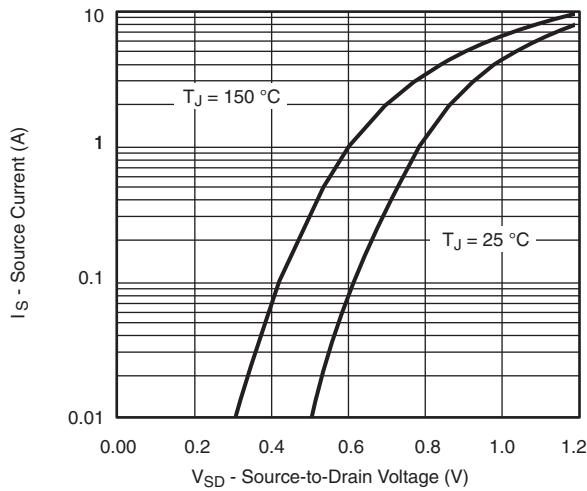
SCHOTTKY SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Forward Voltage Drop	V_F	$I_F = 1\text{ A}$		0.45	0.50	V
		$I_F = 1\text{ A}, T_J = 125\text{ }^\circ\text{C}$		0.36	0.42	
Maximum Reverse Leakage Current	I_{rm}	$V_R = 30\text{ V}$		0.04	0.1	mA
		$V_R = 30\text{ V}, T_J = 75\text{ }^\circ\text{C}$		0.1	2	
		$V_R = 30\text{ V}, T_J = 125\text{ }^\circ\text{C}$		2	10	
Junction Capacitance	C_T	$V_R = 10\text{ V}$		62		pF

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

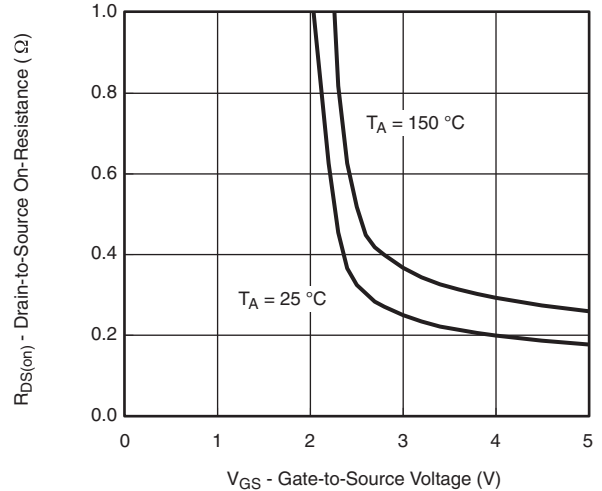
MOSFET TYPICAL CHARACTERISTICS $T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted



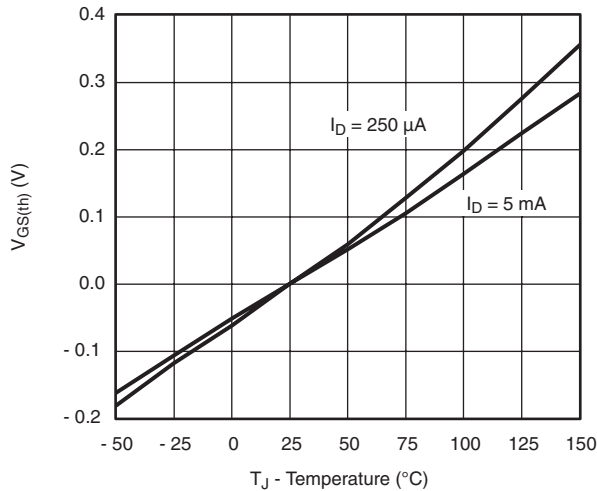
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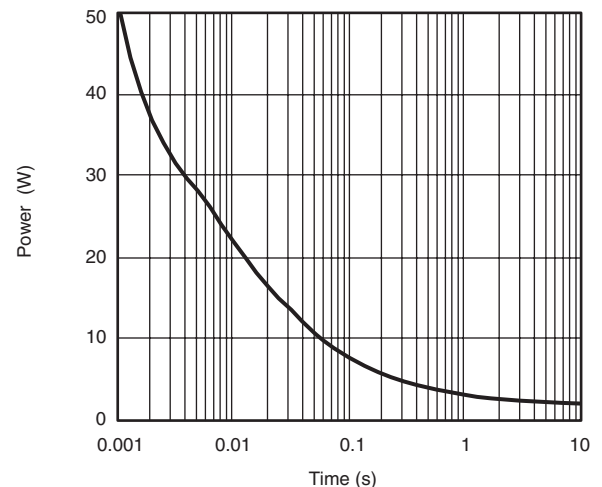
Source-Drain Diode Forward Voltage



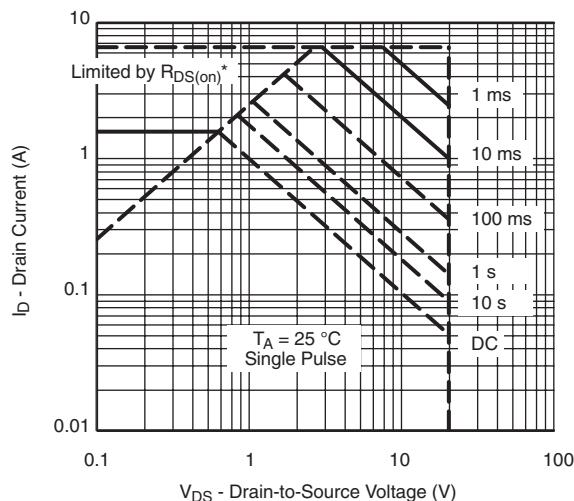
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

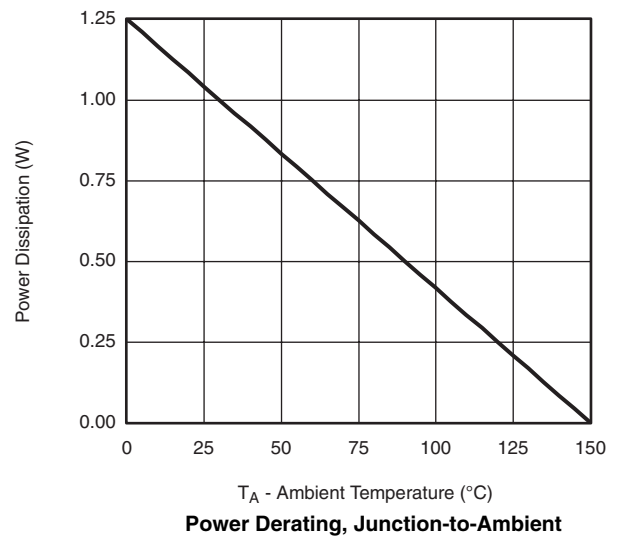
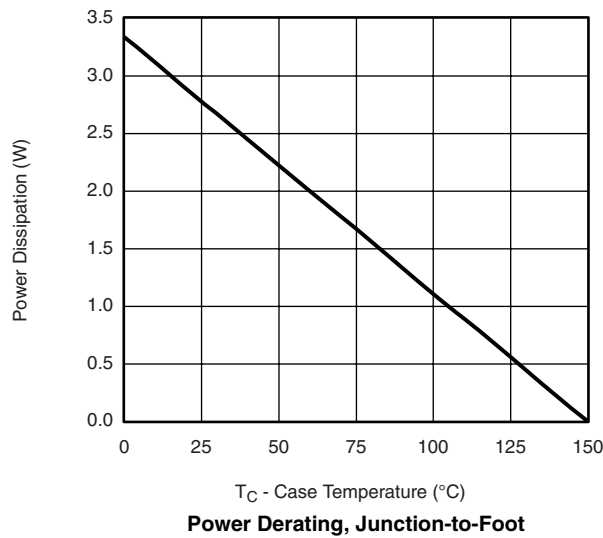
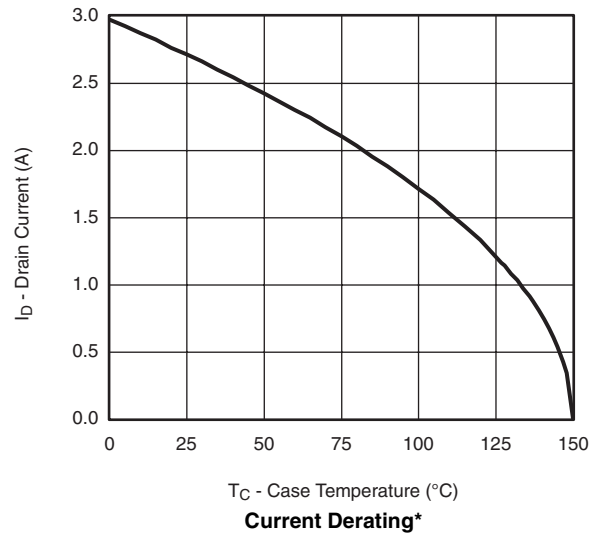


Single Pulse Power, Junction-to-Ambient



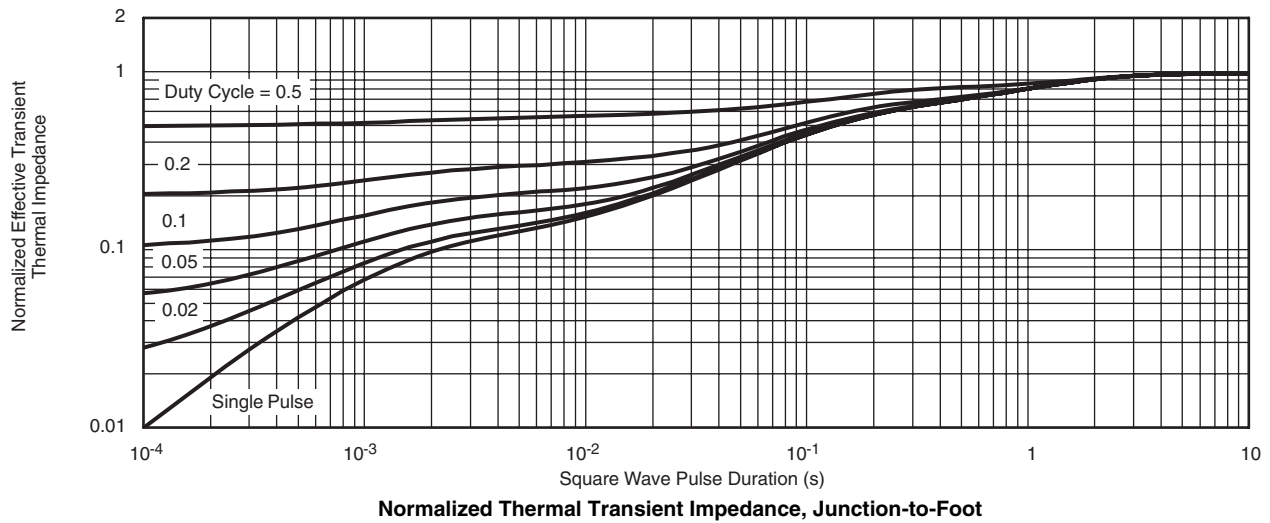
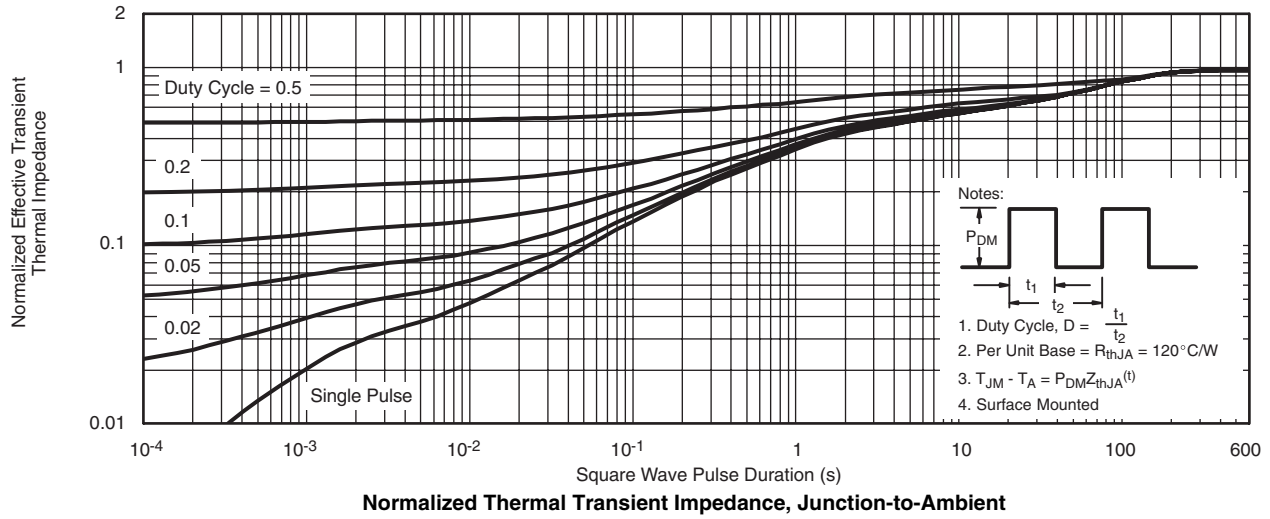
Safe Operating Area, Junction-to-Ambient

MOSFET TYPICAL CHARACTERISTICS $T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted

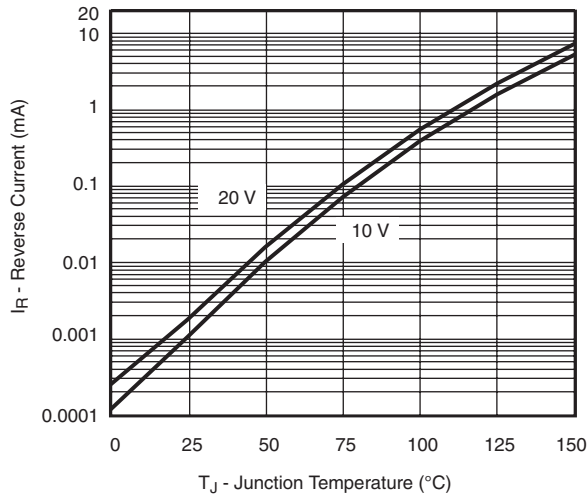


* The power dissipation P_D is based on $T_{J(\max)} = 150\text{ }^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

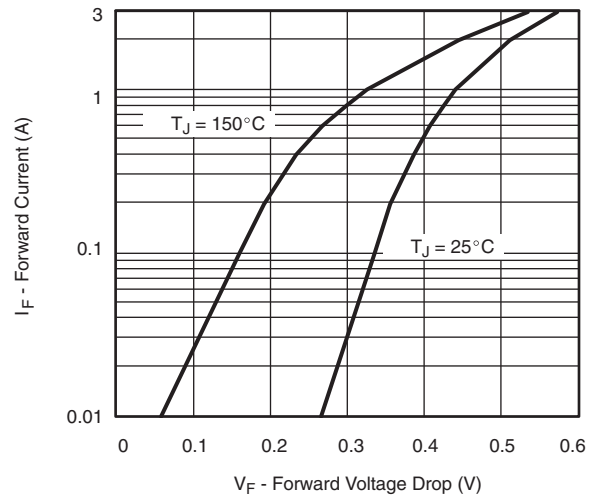
MOSFET TYPICAL CHARACTERISTICS $T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted



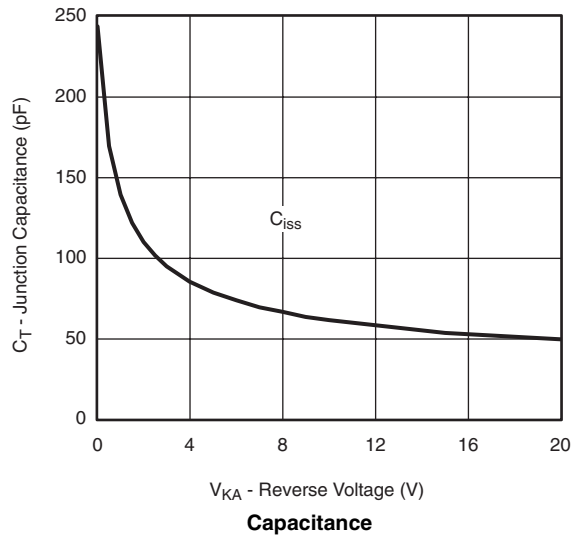
SCHOTTKY TYPICAL CHARACTERISTICS $T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted



Reverse Current vs. Junction Temperature



Forward Voltage Drop



Capacitance

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