PCN Number:	20211220004.2	**PCN Date:**	December 22, 2021
Title:	Qualification of new Fab site (CFAB) using qualified Process Technology, Die Revision, Probe & final test site, and additional Assembly site/BOM options for select devices		
Customer Contact:	PCN Manager	**Dept:**	Quality Services
Proposed 1st Ship Date:	June 22, 2022	**Estimated Sample Availability:**	Date provided at sample request.

Change Type:

☒	Assembly Site	☒	Assembly Process	☒	Assembly Materials
☒	Design	☐	Electrical Specification	☐	Mechanical Specification
☒	Test Site	☐	Packing/Shipping/Labeling	☐	Test Process
☐	Wafer Bump Site	☐	Wafer Bump Material	☐	Wafer Bump Process
☒	Wafer Fab Site	☒	Wafer Fab Materials	☒	Wafer Fab Process
		☒	Device Symbolization		

PCN Details

Description of Change:

Texas Instruments is pleased to announce the qualification of a new fab & process technology, (CFAB, JI3), die revisions, probe and final test site, and Assembly (TIPI) site for selected devices as listed below in the product affected section. Construction differences are noted below:

Current Fab Site			Additional Fab Site		
Current Fab Site	**Process**	**Wafer Diameter**	**Additional Fab Site**	**Process**	**Wafer Diameter**
SFAB	JI1	150 mm	CFAB	JI3	200 mm

The die was also changed as a result of the process change.

Probe site change:

	Current:	**New:**
Probe Site & Final Test	TI Sherman-Probe (SH-BIP) & UTL2	**n/a &TIPI**

Construction differences are noted below:

	Current – UTL2	**New - TIPI**
Lead finish	NiPdAu, non RLF	NiPdAu, RLF
Mold Compound	SID#CZ0096	4222198
Bond wire diameter	Au, 1.0 mils	Cu, 0.80 mil
Mount Compound	SID#PZ0013	8095733
Device marking - Pin 1 ID	Stripe	Dot
Device Marking -Traceability	Standard binary	2 level binary

Reason for Change:

These changes are part of our multiyear plan to transition products from our 150-milimeter factories to newer, more efficient manufacturing processes and technologies, underscoring our commitment to product longevity and supply continuity.

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

Impact on Environmental Ratings

Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.

RoHS	**REACH**	**Green Status**	**IEC 62474**
☒ No Change	☒ No Change	☒ No Change	☒ No Change

Changes to product identification resulting from this PCN:

Fab Site Information:

Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City
SH-BIP-1	SHE	USA	Sherman
CFAB	**CU3**	**CHN**	**Chengdu**

Die Rev:

Current	**New**
Die Rev [2P]	**Die Rev [2P]**
A	**A**

Assembly Site Information:

Assembly Site	Assembly Site Origin (22L)	Assembly Country Code (23L)	Assembly City
UTL2	NS2	THA	Bangpakong, Chachoengsao
TIPI	**PHI**	**PHL**	**Baguio City**

Sample product shipping label (not actual product label)

Product Affected:

SN331QDBVRQ1	TL331IDBVRQ1	TL331QDBVRQ1

Automotive New Product Qualification Summary
(As per AEC-Q100 and JEDEC Guidelines)

TL331Q Red Bull
Approve Date 11-OCTOBER -2021

Product Attributes

Attributes	Qual Device: TL331QDBVRQ1	QBS Reference: LM2904BQDRQ1	QBS Reference: TL391BQDBVRQ1
Automotive Grade Level	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125
Product Function	Signal Chain	Signal Chain	Signal Chain
Wafer Fab Supplier	CFAB	CFAB	CFAB
Assembly Site	PHI	FMX	PHI
Package Group	SOT	-	SOT
Package Designator	DBV	D	DBV
Pin Count	5	8	5

- QBS: Qual By Similarity
- Qual Device TL331QDBVRQ1 is qualified at MSL1 260C

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: TL331QDBVRQ1	QBS Reference: LM2904BQDRQ1	QBS Reference: TL391BQDBVRQ1
Test Group A - Accelerated Environment Stress Tests										
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	-	1/308/0	-	2/616/0
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	-	1/12/0	-	2/24/0
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	-	-	-
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C	96 Hours	-	-	-
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	1/77/0	-	2/154/0
UHST	A3	JEDEC JESD22-A118	3	77	Unbiased HAST	130C	192 Hours	-	-	-
UHST	A3	JEDEC JESD22-A118	3	77	Unbiased HAST	130C/85%RH	96 Hours	1/77/0	-	2/154/0
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65/150C	500 Cycles	-	-	-
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	1/77/0	-	2/154/0
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	1000 Hours	1/45/0	-	2/90/0
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	175C	500 Hours	-	-	-

Test Group B - Accelerated Lifetime Simulation Tests										
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	125C	1000 Hours	1/77/0	-	2/154/0
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	150C	408 Hours	-	-	-
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate	125C	48 Hours	-	3/2400/4[1]	-
Test Group C - Package Assembly Integrity Tests										
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	-	2/60/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	-	2/60/0
SD	C3	JEDEC JESD22-B102	1	15	PB Solderability	>95% Lead Coverage	-	1/15/0	-	-
SD	C3	JEDEC JESD22-B102	1	15	PB-Free Solderability	>95% Lead Coverage	-	1/15/0	-	-
PD	C4	JEDEC JESD22-B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	1/10/0	-	2/20/0
Test Group D - Die Fabrication Reliability Tests										
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements

Test Group E - Electrical Verification Tests										
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	1/3/0	-	1/3/0
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	500 Volts	1/3/0	-	1/3/0
LU	E4	AEC Q100-004	1	6	Latch-Up	Per AEC Q100-004	-	1/6/0	-	1/6/0
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	1/30/0	-	2/60/0

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E): -40C to +150C
- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

TI Qualification ID: R-CHG-2109-043

[1] ELFR fails due to a defect screenable at production test.

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